

Migrating from an existing MVI56-HART to PLX51-HART-4I Document Code: TN-PLX51HART4I\_Migrating from MVI56\_69-HART to PLX51-HART-4I-2002 Date: February 7, 2020 Revision: 02

Applicable products include:

Converting from:

• MVI56-HART



Converting to:

PLX51-HART-4I



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ProSoft Technology, Inc. PLX51-HART-4I-2002 TN-PLX51HART4I\_Migrating from MVI56\_69-HART to



Note that this document can also be used as a reference guide for migrating from MVI69-HART to PLX51-HART-4I.

Migrating from an existing MVI56-HART to the new PLX51-HART-4I, you will require to have an EtherNet/IP bridging module such as 1756-ENBT/EN2T/EN2TR on the controller chassis. The gateway will require a vacant IP address in the same subnet as the EtherNet/IP bridging module. The migration can be simple but depends on the existing configuration, it can be not straight forward. These modules use different configuration software for the configuration, but both remain easy to use.

It is highly recommended to review the <u>PLX51-HART-4I training video</u> on ProSoft Technology's YouTube channel.

- MVI56-HART uses **ProSoft Configuration Builder** (aka PCB)
- PLX51-HART-4I uses ProSoft PLX50 Configuration Utility

You can download the tools free of charge on our website:

- ProSoft Configuration Builder
- ProSoft PLX50 Configuration Utility

The PLX51-HART-4I is built with 4 HART channels, for each channel it do support multi-drop connection up to 2 devices when connecting in series (the series connection method has the advantage of the (4-20 mA) current still being controlled by one of the devices, which may be required in some applications) and up to 7 when connecting in parallel. MVI56-HART was built with 4 HART channels but it supports up to 15 devices per channel in multi-drop connection. Additional PLX51-HART-4I units and rewiring works will be required if the limit exceeds.

PLX51-HART-4I module capable to convert HART signal into either EtherNet/IP, Modbus TCP/IP, or DNP3 TCP/UDP protocols. Only one conversion can be selected per module. In this migration technical note, we will be covering on EtherNet/IP Logix conversion only.

Besides, additional features/functions are available on PLX51-HART-4I:

- FTD-DTM compatible for Asset Management Systems
- (require PLX51-HART DTM Pack Configuration Software)
- Ethernet configuration and diagnostic
  - HART device discovery, configuration, status, statistics, trends...
  - HART analog input calibration

#### Audience:

You would be interested in this Technical Note if you are currently using Rockwell ControlLogix in-chassis HART module MVI56-HART or CompactLogix in-chasis HART module MVI69-HART to collect analog values or HART digital data from HART sensors.



#### Migrating MVI56-HART to PLX51-HART-4I

Before going into the configuration step, you will like to look into the wiring, module setup, IP address setup of PLX51-HART-4I from the <u>User Manual</u>. Set the module to a vacant IP address on your EtherNet/IP network.

Then, study the configuration of your MVI56-HART by opening the backup PCB file. If you do not have a backup file, you may upload your existing configuration from your module using PCB.

1. Launch **PCB** and create a new project. Select **MVI56** under *Product Line Filter* and **MVI56**-**HART** under *Module Type*. Then click **OK**.

Choose Mod	ule Type					$\times$
		Produc	t Line Filter			
C All	C PLX4000 C PLX5000 C MVI69E	<ul> <li>C PLX6000</li> <li>C PLX30</li> <li>C MVI69L</li> </ul>	C MVI46 C MVI69 C PLX80		C MVI71 C PTQ	
		Search	Module Type			_
STEP 1: 5	Select Module Ty	уре	Module Definit	ion:		
MVI56-H	IART Define Ports	<u>•</u>	HART MULTI-E MODULE	DROP COMMUN	ICATION	
Sectio	n	Status	Acti	on Required		I
✓ Module Used		Used	UnC	heck if Not Use	d	
✓ HART Port 0 Used		Used				
V HAI	RT Port 1	Used				
	RT Port 2	Used				
✓ nai	mment	Used				
				ОК	Cancel	

2. Right click on MVI56-HART from Project pane on the left. Click Upload from Device to PC.



Migrating from an existing MVI56-HART to PLX51-HART-



3. You will need to connect your PC to the Configuration (CFG) port using the RJ45-DB-9 Serial Adapter Cable and the Null Modem Cable (included in the package with the MVI56-HART module). If you do not have a serial port on your PC, you will need a USB-to-RS-232 Adapter. Check the running COM port number from *Device Manager*.

击 Device Manager
File Action View Help
🗢 🔶 🗊 🛛 🖬 💭
V 🛱 Ports (COM & LPT)
MOXA USB Serial Port (COM1)

Select the COM port number in PCB Upload window and click **UPLOAD**.

Upload files from module to PC	×
 _ STEP 1: Select Communication Path:	
Select Connection Type:	Browse Device(s)
Ethernet: · · ·	Use Default IP
CIPconnect:	CIP Path Edit
	RSWho
STEP 2: Transfer File(s):	
UPLOAD Abort	Test Connection
OK	Cancel

4. After upload complete, you may look into the existing configuration such as **Module Setting**, **Port Setting** and **Port Commands** by clicking the MVI56-HART icon or by section with setting description from respective icon.



· · · · ·							
🖻 🖬   🕇 🗕 🖄 🐿 🔶 🖬 🎯 .							
Default Project	Name	Status	Information				
Default Location	VI56-HART	Configured	MVI56-HART				
mvi56-HART	MVI56	HRT5	2.07				
🖻 🖧 Module	Module	Values OK					
Module	HART Port 0	Values OK					
⊟ sa HART Port 0	HART Port 1	Values OK					
HARI Port 0	<	VALUES CIK					>
HART Port 0 Commands							_
	# Module Information						- 1
	# Last Change: Never						
	# Last Download: Never						
timent	# Application Rev:						
	# Loader Rev:						
	# MAC Address: # ConfigEdit Version: 4	4 22 27					
	" configure version. 4						
	# Module Configuration						
	[Module]						
	Module Type : MVI56-HAR	T					
	Module Name . MV130-HAR						
	and and the state		# charter pp addres				
	Read Register Start	: 800	# Starting DB addres # Number of regs for	s where read by pi processor to rea	d		
	Write Register Start	: 800	# Starting DB addres	s where write dat	a placed		
	Write Register Count	: 400	<pre># Number of regs to # Location of port @</pre>	write to module fi error/status offse	rom processor		
	Failure Flag Count	: 0	# Determines if BP f	failure will cause	protocol to b	e	
	Initialize Õutput Data	: NO					
	[HART Port 0]						
	Enabled	: Yes	# Y=Use port, N=Do r	not use port			
	Preambles Primary Master	: 5 • Ves	<pre># Number of Preamble # Y=The Master is Pr</pre>	es to be sent to H/	ART Network		
	Retry Count	: 3	# Number of retries	after error			
	DB Address Status	: 3600	# Database Address t	to place device st	atus		
	Auto-Poll Code	: 3 : p2p	<pre># Number of Commands # P=p2p. M=multidrop</pre>	5 in Command Table 5. N=Not used			
	Auto-Poll DB Address	: 5	# DB address for aut	to-poll data			
	Auto-Poll Swap Float	: Word and By	e Swap # Swap code for # Maximum device add	r auto-poll float ( dress number in th	data e network (1-1	5)	
	Error/Status Offset	-1	" Hax main device add	ar coo number in en		5)	
	[HAPT Port 0 Commands]						
	START						
	# Enable	Float DB Ad	dress Poll Interval	FP Word Count	FP Swap Code		S
	UI DISAbled	0	0	0	No change		- 1

Edit - Module		×
Read Register Start Read Register Count Write Register Start Write Register Count Error/Status Offset Failure Flag Count Initialize Output Data	0 800 400 3800 0 No	Read Register Count         800         Comment:         Number of regs for processor to re         Definition:         Number of regs to read from module for processor.



- 5. Launch ProSoft PLX50 Configuration Utility and create a new project.
  - ProSoft PLX50 Configuration Utility
    File Device Tools Window Help
    New
    Open
    Close
    Save
    Save
    Save
    Recent •
    Exit
- 6. Then add a PLX51-HART-4I by selecting Add under the *Device* menu, and click Ok.



🚸 Add New Device X					
Select Device Type					
Image	Device Name	Description			
	DF1 Messenger	DF1 Messenger Communication Module			
	DF1 Router	DF1 to Logix Communication Module			
	PLX51-DL-232	Data Logger Module			
	PLX51-HART-4I	HART 4-Channel Input Communication Module			
	PLX51-HART-40	HART 4-Channel Output Communication Module			
	Ok	Cancel			



7. In the General tab, enter the IP address of your PLX51-HART-4I.

HART 4 In - Configuration	
General Ch 0 Ch 0 - Adv. Ch 1 Ch 1 - Adv. Ch 2 Ch 2 - Adv. Ch 3 Ch 3 - Adv. DNP3 (Disabled)	
Instance Name HART 4 In Description	
IP Address 10 . 194 . 67 . 50 Major Revision 1 🗸	
Protocol EtherNet/IP ~	
Node Address 0	
EtherNet/IP Advanced Diagnostics	
Logix Path 10.194.67.101,1,0	
Ok Apply Cancel	

You can also browse for connected devices by clicking the "..." button on the right of the IP address field.



8. Select **EtherNet/IP** under *Protocol*. If you would like the Advanced Diagnostics to be written to your Logix Controller, click the Logix Path **browse** and select your controller from the list, confirm by clicking **Ok**.

EtherNet/IP Advanc	ed Diagnostics		
* Target E	rowser 10.194.67.50 : PLX51-HART-4I 10.194.67.101 : 1756-EN2T/B 00 : 1756-L61/B LOGIX5561 01 : 1756-EN2T/B 02 : MVI56E-DNPNET	X Done	
	Ok Cancel		

- 9. Next, we will look at the port setting for both Point-to-Point and Multidrop. In the different "Ch. X" tabs, select the right parameters for your application if the defaults do not match.
- a) For example, Port 0 is set as Point-to-Point:

dit - HART Port 0		×
Enabled Preambles Primary Master Retry Count DB Address Status Command Count Auto-Poll Code Auto-Poll DB Address Auto-Poll DB Address Auto-Poll Swap Float Max Device Count Error/Status Offset	Yes 5 Yes 3 3600 3 <b>p2p</b> 0 Word and Byte Swap 1 -1	Auto-Poll Code          p2p         Comment:         P=p2p, M=multidrop, N=Not used         Definition:         Selections for this parameter



The default setting should be sufficient, unless you want to discover the filter and trending features.

General Ch 0 Ch 0 - Adv. Ch 1 Ch 1 - Adv. Ch 2 Ch 2 - Adv. Ch 3 Ch 3 - Adv. DNP3 (Disabled)     Signal   Range 4-20 mA   Raw Max   20 (mA) EU Max 100   Raw Min 4 (mA) EU Min 0     HART Communication   Image Enable Relay Messages (Class 2) Fixed HART Address   PV Update Rate 1 second Adv. Diag.   Trend Defaults Source Filtered Scaled Value Sample Rate 1000
Image       Image <t< td=""></t<>
Signal       Range       4-20 mA       Filter       10       (ms)         Raw Max       20       (mA)       EU Max       100         Raw Min       4       (mA)       EU Min       0         HART Communication
Range       4-20 mA       Filter       10       (ms)         Raw Max       20       (mA)       EU Max       100         Raw Min       4       (mA)       EU Min       0         HART Communication       Image: Class 2       Image: Fixed HART Address         PV Update Rate       1 second       Adv. Diag. Ratio       3       Address       Image: Class 2         Trend Defaults       Source       Filtered Scaled Value       Sample Rate       1000       (ms)
Raw Max       20       (mA)       EU Max       100         Raw Min       4       (mA)       EU Min       0         HART Communication       Image: Class 2       Image: Fixed HART Address         PV Update Rate       1 second       Adv. Diag. Ratio       3       Address       Image: Class 2         Trend Defaults       Source       Filtered Scaled Value       Sample Rate       1000       (ms)
Raw Min       4       (mA)       EU Min       0         HART Communication       Image: Class 2       Image: Fixed HART Address         Image: Enable HART       Image: Enable Relay Messages (Class 2)       Image: Fixed HART Address         PV Update Rate       1 second       Adv. Diag. Ratio       3       Address       Image: Class 2)         Trend Defaults       Image: Source       Filtered Scaled Value       Sample Rate       1000 (ms)
HART Communication
Image: Source       Filtered Scaled Value       Sample Rate       1000       (ms)
PV Update Rate       1 second       Adv. Diag. Ratio       3       Address       0         Trend Defaults
Trend Defaults Source Filtered Scaled Value V Sample Rate 1000 (ms)
Source Filtered Scaled Value V Sample Rate 1000 (ms)
Ok Apply Cancel

b) For example, Port 1 is set as multidrop:

	-	-	
S E	dit - HART Port 1		×
	Enabled	Yes	Enabled
	Preambles	5	·
1	Primary Master	Yes	Yes
	Retry Count	3	
	DB Address Status	3601	
	Command Count	0	Comment:
1	Auto-Poll Code	multidrop	Comment:
11	Auto-Poll DB Address	200	Y=Use port, N=Do not use port
	Auto-Poll Swap Float	Word and Byte Swap	Definition
	Max Device Count	5	Definition.
S	Error/Status Offset	-1	Port enable flag (Yes/No)

In multidrop setting, to ensure the correct field device is used as the "main" field device on the drop (which will be used to populate the Logix input assembly), the user will need to set the **node address** of the specific device. This is done by setting the **Fixed HART Address** parameter in the PLX50 Configuration Utility as shown below:



**Technical Note** Migrating from an existing MVI56-HART to PLX51-HART-4I

♦ HART 4 In - Configuration
General         Ch 0         Ch 0 - Adv.         Ch 1         Ch 1 - Adv.         Ch 2         Ch 2 - Adv.         Ch 3         Ch 3 - Adv.         DNP3 (Disabled)
✓ Enable Channel
Range 4-20 mA V Filter 10 (ms)
Raw Max         20         (mA)         EU Max         100
Raw Min 4 (mA) EU Min 0
HART Communication ☑ Enable HART ☑ Enable Relay Messages (Class 2) ☑ Fixed HART Address
PV Update Rate 1 second ~ Adv. Diag. Ratio 3 Address 1
Trend Defaults
Source Filtered Scaled Value ~ Sample Rate 1000 (ms)
Ok Apply Cancel

c) If the Port in MVI56-HART is not enabled, you may disable it in PLX51-HART-4I by untick the **Enable Channel** box.





10. Open up your existing RSLogix/Studio 5000 program back up copy, delete the **MVI56-HART** module from your 1756 Backplane.

		lases		
Motion Groups	đ	New Module		
Add-On Instructions		Discover Modules		
🖶 🖶 Data Types	Ж	Cut	Ctrl+X	
	Ē	Сору	Ctrl+C	(End
🔤 🙀 Add-On-Defined	ß	Paste	Ctrl+V	I 1
🗄 🚂 Predefined		Delete	Del	I 1
🗄 🖏 Module-Defined 🖳		Cross Reference	Ctrl+E	
		Properties	Alt+Enter	
[] [0] 1756-L61 MVI56		Print	×	
	- IAIN			

11. Delete the **HART** tag from *Controller Tag*, under *Edit Tags* tab.

Scop	e: 🛅 MVI56_HART 🗸 Show: Al	ll Tags		~	Y. Enter Name Fi	hev		
N	ame 📰 🛆 Alias For	Base Tag	Data Type	Description	External Access	Constant	Style	
			HARTModuleDef		Read/Write			
	Monitor "HARI"	I	REAL		Read/Write		Float	_
	New Tag which aliases "HART"							
	Edit "HARI" Properties	Alt+Enter						
	Edit "HARTModuleDef" Data Type							
	Edit "HART" Description	Ctrl+D						
	Go to Cross Reference for "HART"	Ctrl+E						
	Find All "HART"							
	<u>G</u> o To	Ctrl+G						
Ж	Cut	Ctrl+X						
8	Сору	Ctrl+C						
e	Paste	Ctrl+V						
	Paste Pass-Through							
	Delete	Del						
	Expand All "HART" Members	Ctrl+Plus						
	Collapse All "HART" Members							
	Set External Access for "HART"	•						
_								
Þ	Monitor Tags			(				ł



12. Then delete the rung used for MVI56-HART module. The rungs consist of a *NEQ* instruction to *HART* tags, with two *JSR* on **Main Program**, then a **ReadData** and **WriteData** rungs.

Controller Tags  Controller Tags  Controller Pauk Handler  Power-Up Handler  Tasks  MainProgram  Program  Program  Chan  MainRoutine	0	Hot Equal Source A Locat 1: Data[249] Orce B HART.BP LastRead 0 €	JSR- JJSR- JSR- Jump To Subroutine Routine Name WriteData	Î
🗎 ReadData 🛱 WriteData	(End)			_

- 13. You may delete the associate UDT (all as shown below), but this is optional. If you would like to delete them, you will need to delete **HARTModuleDef** first as other UDT are used in this one. Then right click and delete them one by one. If *Delete* are grey out, you will need to delete other UDT first, as it is used elsewhere.
  - Data Types User-Defined HARTAutoPoll HARTBACKPLANE HARTCOMTROL HARTCONTROL HARTCONTROL HARTGetCommandErrorsREQ HARTGetCommandErrorsRESP HARTGetCommandErrorsRESP HARTGetCommandErrorsRESP HARTGetCommandErrorsRESP
- 14. Now add the PLX51-HART-4I module, it must be added to the RSLogix 5000 I/O tree as a *Generic Ethernet module*. This is achieved by right-clicking on *the Ethernet Bridge* (For example 1756-EN2T) in the *1756 Backplane* and selecting *New Module*. Then select **ETHERNET-MODULE** as shown in the figure below.

				Selec	t Mod	dule Type						
				Ca	talog	Module Discovery Favorite	es					
	56- <i>1</i> /156	A7 _HART			gener	ric Module Tvpe	Category Filters	Clear	Filter	3 Module Type Vendor Filter	Hide Filters	*
B- D (1) 1/36-EN21 E	J	I New Module Discover Modules Paste	Ctrl+V		~	Analog CIP Motion Converter Communication Communications Adapter		>	<pre>&gt; &lt; &lt; &lt; &lt; </pre>	Advanced Energy Industries, Inc. Dialight Endress+Hauser FANUC CORPORATION	1	*
		Print	•		Cata E	alog Number ETHERNET-BRIDGE ETHERNET-MODULE	Description Generic EtherNet/IP CIP Generic Ethernet Module	Bridge		Vendor Rockwell Autom Rockwell Autom	Category Communication Communication	
					< 2 of 4	99 Module Types Found					Add to Favo	> ites
						ose on Create				Create	Close	Help



15. You must enter the IP address of your PLX51-HART-4I module. The assembly instance and size must also be added for the input, output, and configuration in the connection parameters section as shown below. You will need to enter the exact connection parameters before the module will establish a Class 1 connection with the Logix controller.

New Module						×
Type: Vendor: Parent: Name:	ETHERNET-MODULE Generic Ethern Rockwell Automation/Allen-Bradley EN2T PLX51HART4I	et Module	ameters Assemblu			
Description:			Instance:	Size:		
		Input:	113	119 🚔	(32-bit)	
-	Y	Output:	116	1	(32-bit)	
Comm Format	: Data - DINT 🛛 🗸 🗸	Configuration:	102	d 🖻	(8-bit)	
Address / H	ost Name	coninguration.			(O Dit)	
IP Addre	ess: 10 . 194 . 67 . 50	Status Input:				
O Host Na	me:	Status Output:				
🗹 Open Modu	ile Properties	OK	Cano	cel	Help	

16. Add the connection requested packet interval (RPI). This is the rate at which the input and output assemblies are exchanged. The recommended value is 200ms.

Module Properties Report: EN2T (ETHERNET-MODULE 1.1)
General Connection Module Info
Requested Packet Interval (RPI): 200.0 Image (1.0 - 3200.0 ms)
Major Fault On Controller If Connection Fails While in Run Mode
Use Unicast Connection over EtherNet/IP



17. In *Main Program*, right click and click **Import Rungs**, the sample ladder can be found on PLX51-HART-4I webpage, <u>download section</u>. (PLX51-HART-4I Multidrop Sample Ladder file)

	X	Cu <u>t</u> Rung	Ctrl+X		
	Þ	<u>C</u> opy Rung	Ctrl+C		
(End)	ß	<u>P</u> aste	Ctrl+V		
(End)		Delete Rung	Del		
		Add Rung	Ctrl+R		
		Ed <u>i</u> t Rung	Enter		
		Edit <u>R</u> ung Comment	Ctrl+D		
		I <u>m</u> port Rungs			
		Export Puper			
		Export Rungs			

#### Click to import the PLX51HART4I\_AOI\_Sample.L5X.

👸 Import Rung	s					×
Look in:	PLX51_HART	「_4I_MultiDrop_Sample_ladder ∨	G 🤌 📂 🖽 -			
<b>_</b>	Name	^	Date modified	Туре	Size	
Quick access	PLX51HART4	4I_AOI_Sample.L5X	11/2/2019 5:46 AM	Logix Designer X	51 KB	
Desktop						
-						
Libraries						
This PC						
1						
Network						
	File name:	PLX51HART4I_AOI_Sample.L5X		~	Import	t
	Files of type:	RSLogix 5000 XML Files (*.L5X)			Canc	el
	Files containing:	🛏 Rungs		~	Help	)
	Into:	🚺 MainRoutine (MainProgram)		~		
	Overwrite Selec	ated Rungs				



18. Then you will need to run thru each tab to make sure they are no overlapping of the *Tags*, *Data Types* and *Other Components*. If you did not delete the **Data Types** earlier, you have to take note that **HARTStatus** are overlapping, you have to opt for **Overwrite** operation. After everything is ok, click **Ok** to confirm.

port Co	ntent:	Carl	Gaura Data Tura	Deferences	_		_			
1	MainTask B MainProgram		rigure Data Type	Hererences				[]		
	MainRoutine (Rungs)	Н	Import Name	Operation	-0	Final Name △		Description		
		$\vdash$	HART4Input	Create		HART4Input				
	🧖 Tags	$\square$	HARTDeviceStat	Create		HARTDeviceStatus				
<u>A</u>	🚟 📶 Data Types	$\vdash$	HARTDiagnostics	Create		HARTDiagnostics				
	Errors Avarpings	$\square$	HARTMessageR	Create		HARTMessageR				
	Litors/ warnings	$\vdash$	HARTMessageR	Create		HARTMessageR				
			HARTModuleSta	Create		HARTModuleStatus				
			HARTStatus	Uverwrite 🗸	1	HARTSTATUS				
		$\square$	Input	Create		Input	••••			
		$\square$	MessageControl	Create		MessageControl	•••			
		$\square$	PLX51HART4I	Create		PLX51HART4I	•••			
		Ш	ProSoftHARTLeg	Create		ProSoftHARTLeg	•••			
		Ш	ProSoftHARTMul	Create		ProSoftHARTMult	•••			
		Ш	ProsoftSTRING16	Create		ProsoftSTRING16	•••			
		Ш	ProsoftSTRING8	Create		ProsoftSTRING8	•••			
			Status	Create		Status	•••			
		4	One or more us is used, importe If the data layo tags using this	er-defined data d tags will be a ut is different, d data type to en	i typ iffec lata sure	es in import content al ted. values for tags using t tag data converts as	lread this (	dy exist in project. If overwritter data type will be converted if p ected.	, existing tags will be affected. I	f existing data ty lost. Check any

19. After import success, you will have the AOI in your Main Program.

-	Man HART4In Module Input Assembly			
	тар пист на точка при Аззопниу	Conv Fi	COP	
		Source Dest P Length	PLX51HART4I:I LX51HART4IN.Input 1	
	HART Command Relay Message	Message Message Control HARTMes	-(EN)	
		PLX51HART4IN.Con	trol.TriggerMessage	

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20. Now you have to change the mapping source from old MVI56-HART Controller Tags to PLX51-HART-4I Controller Tags accordingly. The Multidrop Sample Ladder as of Nov 2<sup>nd</sup> 2019 have included ProSoftHARTLegacy UDT, which has similar data structure as HARTAutoPoll UDT on MVI56. These will ease the migration from MVI56 to PLX51.



21. For multidrop, you may utilize the Advanced mapping features to map data to MVI56Legacy UDT (ProSoftHARTLegacy UDT in Logix). You may create and name new Logix tag as you like but the tag must use ProSoftHARTLegacy UDT. For this example, I am using PLX51HART4INMultidropLegacyPollingCh1 and make it array of 7, for 7 HART devices that each channel can support.

PLX51HART4INMultidropLegacyPollingCh1	ProSoftHARTLegacy[7]
	ProSoftHARTLegacy
E - PLX51HART4INMultidropLegacyPollingCh1[1]	ProSoftHARTLegacy
E - PLX51HART4INMultidropLegacyPollingCh1[2]	ProSoftHARTLegacy
E - PLX51HART4INMultidropLegacyPollingCh1[3]	ProSoftHARTLegacy
	ProSoftHARTLegacy
	ProSoftHARTLegacy
	ProSoftHARTLegacy

22. In PLX50 Configuration Utility, go to Adv tab of your multi-drop channel, put the address of your HART devices and put the Logix tag name accordingly. For example:

HART 4	In - Co	nfigurati	on												
General	Ch 0	Ch 0 - A	dv. (	Ch 1	Ch 1 -	Adv. Ch	2	Ch 2 - Adv. Ch 3 Ch	3 - Adv.	DNP3 (Disable	ed)				
Advar	nced Map	oping													
															Import
		Action		MD In	dex	Addres	s	Descrip	Description			Data Type		Logix Tag	Browse
•	Mulit	-Drop	$\sim$	0	$\sim$	0	$\sim$	-				MVI56Legacy	$\sim$	PLX51HART4INMultidropLegacyPollingCh1[0]	
	Mulit	-Drop	$\sim$	1	$\sim$	3	$\sim$					MVI56Legacy	$\sim$	PLX51HART4INMultidropLegacyPollingCh1[1]	
			$\sim$		$\sim$		$\sim$						$\sim$		

Then you map them accordingly in your Logix program as you do on pointer 20 for p2p application.

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23. Download configuration to the gateway (right click on gateway and select **Download**) from PLX50 Configuration Utility .

24. Once complete, you will be notified that the download was successful.

ightarrow ProSoft PLX50 Configuration $ imes$								
	Download Successful.							
	Ok							

25. In Logix, before complete, click to verify the controller. All the unmapped tags will be shown up in error. You may go through one by one to check the advanced setting that you was using with your MVI56-HART earlier and replace them accordingly with PLX51-HART-4I features.



#### Troubleshooting

When going online with the module, you can access different module and HART status information. It would also allow you comparing HART variables to what you see in your EtherNet/IP tags.

1. Right click on module and select "Go Online".



2. Double click on the different menu elements to display status

HART 4 In - Channel	1 Status							
General Device Info	Device List	Device Status	Device Configuration	Advanced Status	HART Statistics	PV Tracking	Trend	Calibration
Tag			HAR	T Online				
Descriptor								
Manufacturer			Endress Hauser					
Device Type			24					
PV	-	0.001	mi	llibars				
sv	(	0.003	mi					
TV	TV 0.021			llibars				
FV	FV 25.873			es Celsius				



HART 4 In - Channel 0 Status											
General	ral Device Info Device List Device Status		Device Configuration	Advanced Status	MultiDrop	HART Statistics	PV Tracking	V Tracking Trend			
										_	
	MD Index	Ad	Idress	Status	PV	SV	TV	F	V		
	0		0	Online	0.08197863	0	0		0		
	1		2	Online	0.44628	0	0		0		
1											

\_\_\_END OF TECHNICAL NOTE\_\_\_\_\_